

2193<sup>tnw</sup>

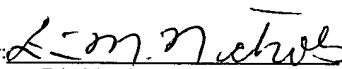
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: DUTTA et al. Examiner: Malzahn, D.  
Serial No.: 10/005,551 Group Art Unit: 2193  
Filed: November 8, 2001 Docket No.: US018181  
(VLSI.331PA)  
Title: HIGH-SPEED COMPUTATION IN ARITHMETIC LOGIC  
CIRCUIT

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 26, 2005.

By:   
Erin M. Nichols

OFFICE ACTION RESPONSE AND AMENDMENT

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the non-final Office Action dated May 20, 2005, please consider the following amendment and remarks.

Amendments to the claims are reflected in the listing of claims beginning at page 2 and the Remarks begin on page 6.